

BOX PCT

IN THE UNITED STATES DESIGNATED/ELECTED OFFICE
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
UNDER THE PATENT COOPERATION TREATY - CHAPTER II

INFORMATION DISCLOSURE STATEMENT
ACCOMPANYING THE FILING OF AN APPLICATION

APPLICANTS(S): HAAS, Peter
ATTORNEY DOCKET NO: P00,1963
INTERNATIONAL APPLICATION NO: PCT/DE99/02086
INTERNATIONAL FILING DATE: 05 JUL 1999
INVENTION: CIRCUIT AND METHOD FOR
CONVERTING DATA IN A PROCESSOR

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

Applicant requests that citation and examination of the following documents
be made during the course of examination of the above-reference application for
United States Letters Patent.

Reference AA	US 5,201,056	06 APR 1993
Reference AJ	JP 61-013-345	21 JANUARY 1986
Reference AK	JP 63-157-241	30 JUN 1988
Reference AL	JP 63-223-833	19 SEP 1988
Reference AM	JP 1-243-126	27 SEP 1989

EXPLANATION OF RELEVANCE

Reference AA discloses a RISC microprocessor architecture with multi-bit tag extended instructions for selectively attaching a tag from either instruction or input data to arithmetic operation output.

Reference AJ discloses a processor for tagged data.

Reference AK discloses a function type processor.

Reference AL discloses an arithmetic unit.

Reference AM discloses an arithmetic and control unit.

Reference AQ is directed to symbolics architecture.

None of the above cited references discloses or suggest a system and method for data conversion in a processor as disclosed and claimed in the present application.

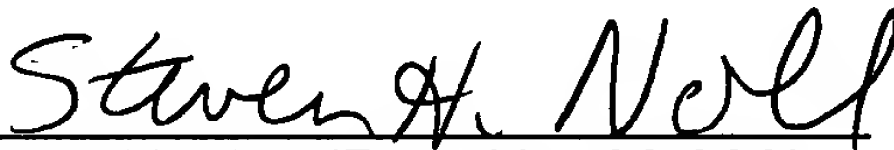
A copy of each reference, together with completed Form PTO 1449, and a copy of the International Search Report, is submitted herewith.

This Information Disclosure Statement is being submitted together with the original application papers, and is therefore in compliance with 37 CFR 1.97(b)(1).

Thus, no fee is necessary.

All claims of this application are submitted to be patentable over the teachings of the above-cited references, taken singularly or in any reasonable combination. Early consideration of the application is therefore respectfully requested.

Respectfully submitted,



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Sheet 1 of 1

Form PTO 1449		U.S. Department of Commerce Patent and Trademark Office			Docket No.: P00,1963		Serial No. New App.	
LIST OF PRIOR ART CITED BY APPLICANT (use several sheets if necessary)					Applicant(s): HAAS, Peter			
					Filing Date Herewith		Group Art Unit Not yet assign.	
U.S. PATENT DOCUMENTS								
Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate	
	AA	5,201,056	Apr. 6, 1993	DANIEL, et al.				
	AB							
	AC							
	AD							
	AE							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AJ	61-013-345	21 JAN 1986	JP				X
	AK	63-157-241	30 JUN 1988	JP				X
	AL	63-223-833	19 SEP 1988	JP				X
	AM	1-243-126	27 SEP 1989	JP				X
	AN							
	AO							
	AP							
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AQ	MOON, David A.; "Symbolics Architecture"; Computer; Jan. 1987; pp. 43-52.						
	AR							
	AS							
	AT							
Examiner			Date Considered					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								